Docket No.: GR 98 P 2191 P

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Date: April 6, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

09/771,886

Confirmation No: 2680

Applicant

Robert Reiner et al.

Filed

January 29, 2001

Title

Clocked Integrated Semiconductor Circuit And Method For

Operating Such A Circuit

Art Unit

2811

Examiner

to be assigned

Docket No. : Customer No. :

GR 98 P 2191 P

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APR 1 3 2004

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 4,932,053 (Fruhauf et al.), dated June 5, 1990, and corresponding Japanese Patent Application JP 2-199561, dated August 7, 1990;

German translation of Japanese Office Action dated February 9, 2004.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

Gregory L. Mayback

Reg. No. 40,719

Date: April 6, 2004

Lerner And Greenberg, P.A. Post Office Box 2480 Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

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Draw line through citation if not in conformance and not considered. Include copy of this form with								
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